

## The Hybrid Pixel Single Photon Counting **Detector** XPAD

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Better results on « low scaterrers » (water)

Design of two different circuits XPAD3 S Collection of holes Single threshold 5-35 keV 10-60 keV

XPAD3 C Collection of electrons Energy window

**Common features** 

The next generation : XPAD3

Pixel size : 130 × 130  $\mu$ m<sup>2</sup>, IBM 0.25  $\mu$ m 120 × 80 pixels / chip : 17 × 10.4 mm<sup>2</sup> 10<sup>7</sup> photons/s/mm<sup>2</sup> Read out time < 2 ms 12 bit counters Counter overflows continuously read out

Planning First prototype : end of 2006

1 Si imager, 8 × 7 chips, 12 × 8 cm<sup>2</sup> + small size CdTe detectors 2.5× 2.5 cm<sup>2</sup>

## Final detectors : mid 2007

3 imagers, Si or CdTe depending on application